

L Number	Hits	Search Text	DB	Time stamp
1	102891	(non-volatile or (flash adj2 memory) or EEPROM) same (memory controller)	USPAT; EPO; JPO; DERWENT	2004/10/21 10:01
2	1137	(non-volatile or (flash adj2 memory) or EEPROM) same (memory controller) same (ECC or (error adj2 correct\$\$ near2 detect\$4) or (error near2 correct\$\$ near2 detect\$4))	USPAT; EPO; JPO; DERWENT	2004/10/21 09:39
3	315	(non-volatile or (flash adj2 memory) or EEPROM) same (memory controller) same (ECC or (error adj2 correct\$4) or EDC or (error near2 correct\$\$ near2 detect\$4))	USPAT; EPO; JPO; DERWENT	2004/10/21 09:40
4	176	same (CPU or computer or processor) ((non-volatile or (flash adj2 memory) or EEPROM) same (memory controller) same (ECC or (error adj2 correct\$4) or EDC or (error near2 correct\$\$ near2 detect\$4)) same (CPU or computer or processor)) and ((memory adj2 system) or (memory adj2 device) or (memory adj2 card))	USPAT; EPO; JPO; DERWENT	2004/10/21 09:41
5	81	((non-volatile or (flash adj2 memory) or EEPROM) same (memory controller) same (ECC or (error adj2 correct\$4) or EDC or (error near2 correct\$\$ near2 detect\$4)) same (CPU or computer or processor)) same ((memory adj2 system) or (memory adj2 device) or (memory adj2 card))	USPAT; EPO; JPO; DERWENT	2004/10/21 09:43
8	1877	controlling adj10 ((non-volatile or (flash adj2 memory) or EEPROM))	USPAT; EPO; JPO; DERWENT	2004/10/21 09:45
9	1073	controlling adj4 ((non-volatile or (flash adj2 memory) or EEPROM))	USPAT; EPO; JPO; DERWENT	2004/10/21 09:44
10	818	controlling adj3 ((non-volatile or (flash adj2 memory) or EEPROM))	USPAT; EPO; JPO; DERWENT	2004/10/21 09:44
11	26	(controlling adj10 ((non-volatile or (flash adj2 memory) or EEPROM))) same (ECC or (error adj2 correct\$\$) or (error adj2 detect\$\$))	USPAT; EPO; JPO; DERWENT	2004/10/21 09:46
12	12	(controlling adj10 ((non-volatile or (flash adj2 memory) or EEPROM))) same (ECC or (error near2 correct\$\$ near2 detect\$\$))	USPAT; EPO; JPO; DERWENT	2004/10/21 09:46
13	568	(memory controller) same (host adj2 interfac\$4) same (memory adj2 interfac\$4)	USPAT; EPO; JPO; DERWENT	2004/10/21 10:02
14	320	(memory controller) same (host adj2 interfac\$4) same (memory adj2 interfac\$4) same ((contorol adj2 circuit) or (control adj2 device) or controller)	USPAT; EPO; JPO; DERWENT	2004/10/21 10:03
15	301	(memory controller) same (host adj2 interface) same (memory adj2 interface) same ((contorol adj2 circuit) or (control adj2 device) or controller)	USPAT; EPO; JPO; DERWENT	2004/10/21 10:04
16	87	((memory controller) same (host adj2 interface) same (memory adj2 interface) same ((contorol adj2 circuit) or (control adj2 device) or controller)) and (ECC or (error near3 correct\$\$ near3 detect\$\$) or (error adj2 correct\$\$))	USPAT; EPO; JPO; DERWENT	2004/10/21 10:05
17	18	((memory controller) same (host adj2 interface) same (memory adj2 interface) same ((contorol adj2 circuit) or (control adj2 device) or controller)) same (ECC or (error near3 correct\$\$ near3 detect\$\$) or (error adj2 correct\$\$))	USPAT; EPO; JPO; DERWENT	2004/10/21 10:05